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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

In re application of:

K. MORI et al.

Group Art Unit: 2818

Application No.: 10/066,603

Examiner: G. Auduong

Filed: February 6, 2002

Docket No.: 100353-00099

For: SEMICONDUCTOR MEMORY DEVICE WITH EFFICIENT AND RELIABLE  
REDUNDANCY PROCESSING

**AMENDMENT UNDER 37 C.F.R. § 1.121**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 8, 2003

Sir:

**INTRODUCTORY COMMENTS**

In reply to the Office Action mailed May 8, 2003, please amend the above-identified application as follows: